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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Garg et al.

Appl. No. 09/188,708

Filed: November 10, 1998

For: RISC Microprocessor Architecture Implementing Multiple Typed

Register Sets

Art Unit: 2783

Examiner: L. Donaghue

Atty. Docket: SP018.C4

Attention: Box Non-Fee Amendment

Third Preliminary Amendment

Assistant Commissioner for Patents Washington, DC 20231

Sir:

Prior to examination of the above-captioned application and calculation of a

enter the following Amendment.

In the Claims:

Please cancel claim 43.

Please amend the following claim 42:

I hereby certify that this paper is being facsimile transmitted to

the Patent and Trademark Office on the date shown below.

Don Featherstone, Registration No. 33,876

Kushville 11/1/a

Signature

Date

MM. 11/2/99